NATIONAL BOARD OF ACCREDITATION

Data Capturing Points of the Program Applied for NBA Accreditation– Tier I/II UG (Engineering) Institute Programs

Program Name : Electronics & Communication Engineering	**Discipline**: Engineering & Technology
Level : Under Graduate	**Tier**: 1
Application No: 11497	**Date of Submission**: 23-10-2026

PART A- Profile of the Institute

A1.Name of the Institute: RAJEEV GANDHI MEMORIAL COLLEGE OF ENGINEERING & TECHNOLOGY	
Year of Establishment : 1995	Location of the Institute: NH40 Nerwada Cross Roads
A2. Institute Address:NANDYAL ANDHRA PRADESH 518501	
City:NANDYAL	State:Andhra Pradesh
Pin Code:518501	Website:www.rgmcet.edu.in
Email:RGMNANDYAL@GMAIL.COM	Phone No(with STD Code):08514-275203
A3. Name and Address of the Affiliating University (if any):	
Name of the University : JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR, ANANTAPUR, A.P	City: Anantpur
State : Andhra Pradesh	Pin Code: 515002
A4. Type of the Institution: Self-Supported Institute	
A5. Ownership Status: Self financing	

A6. Details of all Programs being Offered by the Institution:

- No. of UG programs: **11**
- No. of PG programs: **13**

Table No. A6.1: List of all programs offered by the Institute.

Sr.No.	Discipline	Level of program	Name of the program	Year of Start	Year of Closed	Name of The Department
1	Computer Application	PG	Master in Computer Applications	2006	--	Computer Application
2	Engineering & Technology	UG	Civil Engineering	2008	--	Civil Engineering
3	Engineering & Technology	PG	Computer Aided Structural Engineering	2023	--	Civil Engineering
4	Engineering & Technology	PG	Computer Science	2004	--	Computer Science and Engineering
5	Engineering & Technology	UG	Computer Science and Engineering	1995	--	Computer Science and Engineering
6	Engineering & Technology	UG	Computer Science and Engineering (Artificial Intelligence & Machine Learning)	2022	--	Computer Science and Engineering
7	Engineering & Technology	UG	Computer Science and Engineering (Cyber Security)	2022	--	Computer Science and Engineering

8	Engineering & Technology	UG	Computer Science and Engineering (Data Science)	2020	--	Computer Science and Engineering
9	Engineering & Technology	UG	Computer Science and Engineering and Business Systems	2020	2024	Computer Science and Engineering
10	Engineering & Technology	PG	Digital Systems & Computer Electronics	2004	2018	Electronics and Communication Engineering
11	Engineering & Technology	UG	Electrical and Electronics Engineering	1995	--	Electrical and Electronics Engineering
12	Engineering & Technology	PG	Electrical Drives & Control	2023	--	Electrical and Electronics Engineering
13	Engineering & Technology	UG	Electronics & Communication Engineering	1995	--	Electronics and Communication Engineering
14	Engineering & Technology	UG	Electronics & Instrumentation Engineering	1999	2015	Electronics and Instrumentation Engineering
15	Engineering & Technology	PG	Embedded Systems	2008	2023	Electronics and Communication Engineering
16	Engineering & Technology	PG	Energy Engineering	2023	--	Mechanical Engineering
17	Engineering & Technology	UG	Information Technology	1998	2018	Information Technology
18	Engineering & Technology	PG	Machine Design	2003	2023	Mechanical Engineering
19	Engineering & Technology	UG	Mechanical Engineering	1995	--	Mechanical Engineering
20	Engineering & Technology	PG	Power Electronics	2002	2023	Electrical and Electronics Engineering
21	Engineering & Technology	PG	Software Engineering	2008	2018	Information Technology
22	Engineering & Technology	PG	Structural Engineering	2014	2023	Electrical and Electronics Engineering
23	Engineering & Technology	PG	VLSI Design	2023	--	Electronics and Communication Engineering
24	Management	PG	Masters in Business Administration	2006	--	Management

A7. Programs to be considered for Accreditation vide this Application:

Table No. A7.1: List of programs to be considered for accreditation.

Name of the Department	Having Allied Departments	Name of the Program	Program Level
Electronics and Communication Engineering	No	Electronics & Communication Engineering	UG

Table No. A7.2: Allied Department(s) to the Department of the program considered for accreditation as above.
Cluster ID. Name of the Department (in table no. A7.1) Name of allied Departments/Cluster (for table no. A7.1)

No Record

PART-B: Program information

B1. Provide the Required Information for the Program Applied For:

Table No. B1: Program details.

A. List of the Programs Offered by the Department:

SR.NO.	PROGRAM NAME	PROGRAM APPLIED LEVEL	YEAR OF START / YEAR OF CLOSED	SANCTIONED INTAKE	INCREASE/DECREASE INTAKE (if any)	YEAR OF INCREASE/DECREASE	CURRENT INTAKE	YEAR OF AICTE APPROVAL	AICTE/COMPETENT AUTHORITY ARROVAL DETAILS	ACCREDITATION STATUS	FROM	TO	NO. OF TIMES PROGRAM ACCREDITED	PROGRAM DURATION
1	Electronics & Communication Engineering	UG	1995 / --	60	Yes	2015	240	1995	South-Central/1-2453118471/ 2015/EOA	Granted accreditation for 3 years for the period (specify period)	2023	2026	6	4

List of the Allied Departments/Cluster and Programs:

B2. Detail of Head of the Department for the program under consideration:

A. Name of the HoD :	Dr.J.Sofia Priya Dharshini
B. Nature of appointment:	Regular
C. Qualification:	Ph.D

B3. Program Details

Table No.B3.1: Admission details for the program excluding those admitted through multiple entry and exit points.

Item (Information to be provided cumulatively for all the shifts with explicit headings, wherever applicable)	2025-26 (CAY)	2024-25 (CAYm1)	2023-24 (CAYm2)	2022-23 (CAYm3)	2021-22 (CAYm4)	2020-21 (CAYm5)	2019-20 (CAYm6)
N=Sanctioned intake of the program (as per AICTE /Competent authority)	240	240	240	240	240	240	240
N1=Total no. of students admitted in the 1st year minus the no. of students, who migrated to other programs/ institutions plus no. of students, who migrated to this program	231	239	235	225	228	213	239
N2=Number of students admitted in 2nd year in the same batch via lateral entry including leftover seats	0	24	29	42	37	33	25
N3=Separate division if any	0	0	0	0	0	0	0
N4=Total no. of students admitted in the 1st year via all supernumerary quotas	23	24	24	21	23	17	17
Total number of students admitted in the program (N1 + N2 + N3 + N4) - excluding those admitted through multiple entry and exit points.	254	287	288	288	288	263	281

CAY= Current Academic Year. CAYm1= Current Academic Year Minus 1 CAYm2= Current Academic Year Minus 2. LYG= Last Year Graduate. LYGm1= Last Year Graduate Minus 1. LYGm2= Last Year Graduate Minus 2.

B4. Enrolment Ratio in the First Year

Table No. B4.1: Student enrolment ratio in the 1st year.

Year of entry	N (From Table 4.1)	N1 (From Table 4.1)	N4 (From Table 4.1)	Enrollment Ratio [(N1/N)*100]

2025-26 (CAY)	240	231	23	105.83
2024-25 (CAYm1)	240	239	24	109.58
2023-24 (CAYm2)	240	235	24	107.92

Average [(ER1 + ER2 + ER3) / 3] = 107.78≡ 100

B5. Success Rate of the Students in the Stipulated Period of the Program

Table No.B5.1: The success rate in the stipulated period of a program.

Item	(2021-22) LYG	(2020-21) LYGm1	(2019-20) LYGm2
A*= (No. of students admitted in the 1st year of that batch and those actually admitted in the 2nd year via lateral entry, plus the number of students admitted through multiple entry (if any) and separate division if applicable, minus the number of students who exited through multiple entry (if any).	288.00	273.00	281.00
B=No. of students who graduated from the program in the stipulated course duration	267.00	242.00	269.00
Success Rate (SR)= (B/A) * 100	92.71	88.64	95.73

Average SR of three batches ((SR_1+ SR_2+ SR_3)/3): 92.36

B6. Academic Performance of the First-Year Students of the Program

Table No.B6.1: Academic Performance of the First-Year Students of the Program.

Academic Performance	CAYm1(2024-25)	CAYm2(2023-24)	CAYm3 (2022-23)
Mean of CGPA or mean percentage of all successful students(X)	6.44	6.51	6.27
Y=Total no. of successful students	255.00	256.00	241.00
Z=Total no. of students appeared in the examination	263.00	259.00	245.00
API [X*(Y/Z)]	6.24	6.43	6.17

Average API[(AP1+AP2+AP3)/3] : 6.28

B7: Academic Performance of the Second Year Students of the Program

Table No.B7.1: Academic Performance of the Second Year Students of the Program.

Academic Performance	CAYm1 (2024-25)	CAYm2 (2023-24)	CAYm3 (2022-23)
X=(Mean of 2nd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 2rd year/10)	6.47	5.51	5.88
Y=Total no. of successful students	277.00	277.00	281.00
Z=Total no. of students appeared in the examination	285.00	283.00	287.00
API [X * (Y/Z)]	6.29	5.39	5.76

Average API [(AP1 + AP2 + AP3)/3] : 5.81

B8. Academic Performance of the Third Year Students of the Program

Table No.B8.1: Academic Performance of the Third Year Students of the Program

Academic Performance	CAYm1 (2024-25)	CAYm2 (2023-24)	CAYm3 (2022-23)
X=(Mean of 3rd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 3rd year/10)	6.54	7.27	7.43
Y=Total no. of successful students	267.00	270.00	247.00

Z=Total no. of students appeared in the examination	277.00	281.00	250.00
API [X*(Y/Z)]:	6.30	6.99	7.34

Average API [(AP1 + AP2 + AP3)/3] : 6.88

B9. Placement, Higher Studies, and Entrepreneurship

Table No.B9.1: Placement, higher studies, and entrepreneurship details.

Item	LYG (2021-22)	LYGm1(2020-21)	LYGm2(2019-20)
FS*=Total no. of final year students	277.00	273.00	269.00
X=No. of students placed	189.00	187.00	186.00
Y=No. of students admitted to higher studies	3.00	2.00	2.00
Z= No. of students taking up entrepreneurship	0.00	0.00	0.00
Placement Index(P) = (((X + Y + Z)/FS) * 100):	69.31	69.23	69.89

Average Placement Index = (P_1 + P_2 + P_3)/3: 69.48 Placement Index Points:

PART C: Faculty Details in Department and Allied Departments

(Data to be filled in for the Department and Allied Departments)

C1. Faculty details of Department and Allied Departments

Table No.C1: Faculty details in the Department for the past 3 years including CAY

Sr.No	Name of the Faculty	PAN No.	Highest degree	University	Area of Specialization	Date of Joining in this Institution	Experience in years in current institute	Designation at Time Joining in this Institution	Present Designation	The date on which Designated as Professor/ Associate Professor if any	Nature of Association (Regular/ Contract/ Ad hoc)	Currently Associated (Y/N)	In case of NO, Date of Leaving	IS HOD?
1	Dr.J.Sofia Priya Dharshini	XXXXXXX68P	Ph.D	JNTUA, Anantapuramu	Wireless Communications & Networks	01/07/2005	20.7	Assistant Professor	Professor	01/07/2024	Regular	Yes		Yes
2	Dr.D.Satya Narayana	XXXXXXX27D	Ph.D	JNTU, Hyderabad	Speech Signal Processing	22/07/1996	29.5	Lecturer	Professor	16/05/2008	Regular	Yes		No
3	Dr.A.Sathish	XXXXXXX25E	Ph.D	JNTU, Hyderabad	Low power VLSI	20/06/2003	22.6	Assistant Professor	Professor	20/10/2015	Regular	Yes		No
4	Dr.P.V.Gopi Krishna Rao	XXXXXXX20C	Ph.D	JNTUK, Kakinada	Robust Control Systems	05/07/2002	23.6	Assistant Professor	Professor	20/10/2015	Regular	Yes		No
5	Dr.K.Mallikarjuna	XXXXXXX52D	Ph.D	JNTUK, Kakinada	Image Compression	16/06/2003	21	Assistant Professor	Professor	20/01/2017	Regular	No	29/06/2024	No
6	Dr.V.N.V.Satya Prakash	XXXXXXX32D	Ph.D	JNTUK, Kakinada	Image processing	01/08/2001	24.5	Assistant Professor	Professor	01/01/2019	Regular	Yes		No
7	Dr.S.Govindarajulu	XXXXXXX44R	Ph.D	JNTU, Hyderabad	VLSI	03/05/2024	1.8	Professor	Professor	03/05/2024	Regular	Yes		No

8	Dr.R.Hanuma Naik	XXXXXXX73L	Ph.D	JNTUA, Anantapuramu	Multivariable control system	01/07/2006	19.6	Assistant Professor	Associate Professor	01/01/2015	Regular	Yes		No
9	Dr.C.Venkataiah	XXXXXXX45N	Ph.D	JNTUK, Kakinada	VLSI interconnects	01/07/2009	16.6	Assistant Professor	Associate Professor	03/03/2022	Regular	Yes		No
10	Dr.N.Ramanjaneyulu	XXXXXXX94P	Ph.D	JNTUK, Kakinada	VLSI	01/09/2004	21.4	Assistant Professor	Associate Professor	20/12/2011	Regular	Yes		No
11	Dr.M.Chennakesavulu	XXXXXXX48D	Ph.D	JNTUA, Anantapuramu	Low power VLSI	15/09/2004	21.4	Assistant Professor	Associate Professor	20/12/2011	Regular	Yes		No
12	Dr.N.Nagaraja Kumar	XXXXXXX94P	Ph.D	JNTUK, Kakinada	Image Processing	22/09/2005	20.3	Assistant Professor	Associate Professor	28/12/2009	Regular	Yes		No
13	Dr.Y.Madhushdhana Reddy	XXXXXXX24G	Ph.D	KLU, Vijayawada	Image Processing	22/06/2007	18.6	Assistant Professor	Associate Professor	01/01/2015	Regular	Yes		No
14	Dr.T.Suman	XXXXXXX61A	Ph.D	JNTUA, Anantapuramu	Signal Procesing	18/07/2024	1	Associate Professor	Associate Professor	18/07/2024	Regular	No	30/07/2025	No
15	Dr.S.V.Ratan Kumar	XXXXXXX81J	Ph.D	KLU, Vijayawada	VLSI	01/07/2011	14.6	Assistant Professor	Assistant Professor		Regular	Yes		No
16	Dr.Sayedu Khasim Noorbasha	XXXXXXX39L	Ph.D	Pandicherry University, Pandicherry	Signal Procesing	20/07/2022	3.5	Assistant Professor	Assistant Professor		Regular	Yes		No
17	Dr.D.Usen	XXXXXXX31A	Ph.D	VIT, AP	Image processing	14/11/2022	3.1	Assistant Professor	Assistant Professor		Regular	Yes		No
18	Dr.M.Krishna Reddy	XXXXXXX09F	Ph.D	KLU, Vijayawada	Wireless Communications & Networks	01/01/2025	1	Assistant Professor	Assistant Professor		Regular	Yes		No
19	Mr.J.Leela Mahendra Kumar	XXXXXXX31Q	M.Tech	JNTUA, Anantapur	DSCE	11/06/2008	17.7	Assistant Professor	Assistant Professor		Regular	Yes		No
20	Mr.S.Abdul Rahim	XXXXXXX27D	M.Tech	JNTUA, Anantapur	DSCE	01/07/2010	15.3	Assistant Professor	Assistant Professor		Regular	No	22/10/2025	No
21	Ms.N.Fouzia Sulthana	XXXXXXX53E	M.Tech	JNTUA, Anantapur	Embedded Systems	23/07/2010	15.5	Assistant Professor	Assistant Professor		Regular	Yes		No
22	Mr.P.Chenadra Sekhar	XXXXXXX44D	M.Tech	JNTU, Hyderabad	DSCE	02/07/2012	13.6	Assistant Professor	Assistant Professor		Regular	Yes		No
23	Mr.S.Kashif Hussain	XXXXXXX63B	M.Tech	SKD University, Anantapur	C&SP	01/07/2013	12.6	Assistant Professor	Assistant Professor		Regular	Yes		No
24	Smt.V.Saraswathi	XXXXXXX13J	M.Tech	JNTU, Hyderabad	DECS	01/07/2015	10.6	Assistant Professor	Assistant Professor		Regular	Yes		No
25	Mr.P.Rangappa	XXXXXXX01N	M.Tech	JNTUA, Anantapur	DSCE	01/07/2015	10.6	Assistant Professor	Assistant Professor		Regular	Yes		No
26	Mr.M.V.Raja Sekhar	XXXXXXX94M	M.Tech	SKD University, Anantapur	Commu. & Signal Processing	04/07/2016	9.6	Assistant Professor	Assistant Professor		Regular	Yes		No

27	Smt.M.Maheswari	XXXXXXX39G	M.Tech	JNTUA, Anantapur	Embedded Systems	19/06/2017	8.6	Assistant Professor	Assistant Professor		Regular	Yes		No
28	Smt.N.Lakshmi Prasanna	XXXXXXX46G	M.Tech	JNTUA, Anantapur	DSCE	08/01/2018	8	Assistant Professor	Assistant Professor		Regular	Yes		No
29	Smt.M. Prasanthi	XXXXXXX68K	M.Tech	JNTUA, Anantapur	DSCE	06/02/2020	5.11	Assistant Professor	Assistant Professor		Regular	Yes		No
30	Mr.K.Masthanvali	XXXXXXX82J	M.Tech	JNTUA, Anantapur	Embedded Systems	07/02/2020	5.11	Assistant Professor	Assistant Professor		Regular	Yes		No
31	Smt.K.Mounika	XXXXXXX62D	M.Tech	JNTUA, Anantapur	Embedded System	07/02/2020	5.11	Assistant Professor	Assistant Professor		Regular	Yes		No
32	Mr.S.Lakshmi Venkata Krishna	XXXXXXX82J	M.Tech	JNTUA, Anantapur	DECS	07/02/2020	5.11	Assistant Professor	Assistant Professor		Regular	Yes		No
33	Mr.C.Dasthagiraiah	XXXXXXX15H	M.Tech	JNTUA, Anantapur	DSCE	10/02/2020	5.11	Assistant Professor	Assistant Professor		Regular	Yes		No
34	Mr.P.Ananda Rao	XXXXXXX97C	M.Tech	JNTUA, Anantapur	VLSI System Design	10/02/2020	5.11	Assistant Professor	Assistant Professor		Regular	Yes		No
35	Smt. R.Sireesha	XXXXXXX86K	M.Tech	JNTU, Hyderabad	Embedded Systems	04/03/2021	4.10	Assistant Professor	Assistant Professor		Regular	Yes		No
36	Mr.T.Tirumalesh	XXXXXXX51P	M.Tech	JNTUA, Anantapur	DSCE	01/04/2021	3.4	Assistant Professor	Assistant Professor		Regular	No	31/07/2024	No
37	Smt.B.Indu	XXXXXXX58N	M.Tech	JNTUA, Anantapur	Embedded Systems	01/12/2021	4.1	Assistant Professor	Assistant Professor		Regular	Yes		No
38	Smt.G.I.Lakshmi Prasanna	XXXXXXX43A	M.Tech	JNTUA, Ananthapuramu	Embedded Systems	16/04/2022	3.8	Assistant Professor	Assistant Professor		Regular	Yes		No
39	Smt.D.Sandhya Rani	XXXXXXX10J	M.Tech	JNTUA, Ananthapuramu	VLSI	18/04/2022	3.8	Assistant Professor	Assistant Professor		Regular	Yes		No
40	Smt.G.Yashaswini	XXXXXXX17J	M.Tech	JNTUA, Ananthapuramu	VLSI	08/07/2022	3.6	Assistant Professor	Assistant Professor		Regular	Yes		No
41	Mr.D.Rajesh Setty	XXXXXXX40G	M.Tech	JNTU, Hyderabad	Instrumentation control Systems	08/07/2022	3.6	Assistant Professor	Assistant Professor		Regular	Yes		No
42	Smt.B.Nazma	XXXXXXX35P	M.Tech	JNTUA, Ananthapuramu	VLSI	18/07/2022	3.5	Assistant Professor	Assistant Professor		Regular	Yes		No
43	Smt.G.Bindhu Bhargavi	XXXXXXX37K	M.Tech	JNTUA, Ananthapuramu	Communication & Signal Processing	19/07/2022	3.5	Assistant Professor	Assistant Professor		Regular	Yes		No
44	Mr.K.Anil Kumar	XXXXXXX60N	M.Tech	JNTUA, Ananthapuramu	Communication & Signal Processing	20/07/2022	3.5	Assistant Professor	Assistant Professor		Regular	Yes		No
45	Mr.K.Nagendra Kumar	XXXXXXX18P	M.Tech	JNTUA, Ananthapuramu	VLSI System Design	08/08/2022	3.5	Assistant Professor	Assistant Professor		Regular	Yes		No
46	Mr.Shaik Asif Basha	XXXXXXX85B	M.Tech	JNTUH, Hyderabad	VLSI System Design	10/10/2022	3.3	Assistant Professor	Assistant Professor		Regular	Yes		No

47	Mr.M.Anand Vijaya Kamalanath	XXXXXXX39M	M.Tech	JNTUH, Hyderabad	DSCE	10/10/2022	2.8	Assistant Professor	Assistant Professor		Regular	No	30/06/2025	No
48	Mr.D.Yovan Snanagan Ponselvan	XXXXXXX58Q	M.Tech	Anna University of Technology, Tirunelveli	Applied Electronics	27/10/2022	3.2	Assistant Professor	Assistant Professor		Regular	Yes		No
49	Mr.P.Mahesh	XXXXXXX86C	M.Tech	Anna University, Chennai	Embedded Systems	27/10/2022	2.8	Assistant Professor	Assistant Professor		Regular	No	10/07/2025	No
50	Smt.B.Geetharani	XXXXXXX89R	M.Tech	JNTUA, Ananthapuramu	DECS	22/02/2023	1.10	Assistant Professor	Assistant Professor		Regular	No	18/01/2025	No
51	Mr.D.Bala Krishna Reddy	XXXXXXX48C	M.Tech	NITC, Calicut, Kerala	Signal Processing	22/02/2023	1.4	Assistant Professor	Assistant Professor		Regular	No	02/07/2024	No
52	Smt.Mancha Maheswari	XXXXXXX84N	M.Tech	JNTUH, Hyderabad	Embedded Systems	13/06/2023	0.9	Assistant Professor	Assistant Professor		Regular	No	30/03/2024	No
53	Smt.B.Bindu	XXXXXXX49E	M.Tech	JNTUA, Ananthapur	VLSI	15/09/2023	2.3	Assistant Professor	Assistant Professor		Regular	Yes		No
54	Smt.B.Sowjanya	XXXXXXX73P	M.Tech	JNTUH, Hyderabad	Systems & Signal Processing	01/02/2024	1.11	Assistant Professor	Assistant Professor		Regular	Yes		No
55	Smt.B.Likhitha	XXXXXXX32P	M.Tech	JNTUA, Anantapur	DSCE	01/01/2025	1	Assistant Professor	Assistant Professor		Regular	Yes		No
56	Mr.N.Vinod Kumar	XXXXXXX07A	M.Tech	JNTUA, Anantapuramu	Embedded Systems	15/04/2025	0.8	Assistant Professor	Assistant Professor		Regular	Yes		No
57	Smt.Y.Saraswathi	XXXXXXX60B	M.Tech	JNTUK, Kakinada	DECS	23/06/2025	0.6	Assistant Professor	Assistant Professor		Regular	Yes		No

Table No.C2: Faculty details of Allied Departments for the past 3 years including CAY.

C2. Student-Faculty Ratio (SFR)

No. of UG(Engineering) programs in Department including allied departments/ clusters (UGn):
UG1=1st UG program
UGn=nth UG program
B= No. of Students in UG 2nd year (ST)
C= No. of Students in UG 3rd year (ST)
D= No. of Students in UG 4th year (ST)
No. of PG (Engineering) programs in Department including allied departments/ clusters (PGm):
PG1=1st PG program.
PGm=mth PG program
A= No. of Students in PG 1st year
B= No. of Students in PG 2nd year
Student Faculty Ratio **(SFR)** = S/F
S= No. of students of all programs in the Department including all students of allied departments/clusters.
No. of students (ST)=Sanctioned Intake (SA)+ Actual admitted students via lateral entry including leftover seats (L) if any (limited to 10 % of SA)
Students who admitted under supernumerary quotas (SNQ, EWS, etc) will not be considered in calculating SFR value. Those students are exempted.
F=Total no. of regular or contractual faculty members (Full Time) in the Department, including allied departments/clusters (excluding first year faculty (The faculty members who have a 100% teaching load in the first-year courses)).

No. of UG Programs in the Department1 No. of PG Programs in the Department2
Table No.C2.1: Student-faculty ratio.

Description	CAY(2025-26)	CAYm1 (2024-25)	CAYm2 (2023-24)
UG1.B	264	264	264
UG1.C	264	264	264
UG1.D	264	264	264
UG1: Electronics & Communication Engineering	**792**	**792**	**792**
PG1.A	18	9	9
PG1.B	9	9	0
PG1: VLSI Design	**27**	**18**	**9**
DS=Total no. of students in all UG and PG programs in the Department	819	810	819
AS=Total no. of students of all UG and PG programs in allied departments	0	0	0
S=Total no. of students in the Department (DS) and allied departments (AS)	**S1=** 819	**S2=** 810	**S3=** 819
DF=Total no. of faculty members in the Department	48	48	48
AF= Total no. of faculty members in the allied Departments	0	0	0
F=Total no. of faculty members in the Department (DF) and allied Departments (AF)	**F1=** 48	**F2=** 48	**F3=** 48
FF=The faculty members in F who have a 100% teaching load in the first-year courses	3	3	3
Student Faculty Ratio (SFR)=S/(F-FF)	**SFR1= 18.20**	**SFR2= 18.00**	**SFR3= 18.20**
Average SFR for 3 years	**SFR= 18.13**		

C3. Faculty Qualification

- Faculty qualification index (FQI) = 2.5 * [(10X +4Y)/RF] where
- X=No. of faculty members with Ph.D. degree or equivalent as per AICTE/UGC norms.
- Y=No. of faculty members with M. Tech. or ME degree or equivalent as per AICTE/ UGC norms.
- RF=No. of required faculty in the Department including allied Departments to adhere to the 20:1 Student-Faculty ratio, with calculations based on both student numbers and faculty requirements as per section C2 of this documents: (RF=S/20).

Table No.C3.1: Faculty qualification.

Year	X	Y	RF	FQ = 2.5 x [(10X + 4Y) / RF)]
2025-26(CAY)	16	32	40.00	18.00
2024-25(CAYm1)	15	33	40.00	17.62
2023-24(CAYm2)	13	35	40.00	16.88

C4. Faculty Cadre Proportion

- Faculty Cadre Proportion is 1(RF1): 2(RF2): 6(RF3)
- RF1= No. of Professors required = 1/9 * No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per C2 of this documents:.
- RF2= No. of Associate Professors required = 2/9 * No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents:.
- RF3= No. of Assistant Professors required = 6/9 * No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents:.
- Faculty cadre and qualification and experience should be as per AICTE/UGC norms.

Table No.C4.1: Faculty cadre proportion details.

Year	Professors		Associate Professors		Assistant Professors	
	Required RF1	Available AF1	Required RF2	Available AF1	Required RF3	Available AF3
2025-26	4.00	6.00	9.00	6.00	27.00	36.00
2024-25	4.00	6.00	9.00	7.00	27.00	35.00
2023-24	4.00	5.00	8.00	6.00	26.00	37.00
Average	RF1=4.00	AF1=5.67	RF2=8.67	AF2=6.33	RF2=26.67	AF2=36.00

C5. Visiting/Adjunct Faculty/Professor of Practice

Table No. C5.1: List of visiting/adjunct faculty/professor of practice and their teaching and practical loads.

(CAYm1)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	Dr. R. Vijayarajan	Associate Professor	VIT, Chennai	Digital Image Processing	24.00
2	Dr. B. P. T. Madhav	Professor	KLU, Vijayawada	Communication Systems	12.00
3	Sri. D. Sai Sathish	Founder & CEO	Indian Servers, Vijayawada	Machine Lerning	24.00

(CAYm2)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	Dr. R. Vijayarajan	Associate Professor	VIT, Chennai	Computer Vision	24.00
2	Sri. D. Sai Sathish	Founder & CEO	Indian Servers, Vijayawada	Machine Learning and its Applications	24.00
3	Sri. Paul Kollanoor Ittoopunny	Principal Engineer	IC Design at Broadcom, Bangalore	VLSI	18.00

(CAYm3)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	Dr. R. Vijayarajan	Associate Professor	VIT, Chennai	Computer Vision	30.00
2	Sri. Paul Kollanoor Ittoopunny	Principal Engineer	IC Design at Broadcom, Bangalore	VLSI	30.00

C6. Academic Research

Table No. C6.1: Faculty publication details.

S.No.	Item	2024-25 (CAYm1)	2023-24 (CAYm2)	2022-23 (CAYm3)
1	No. of peer reviewed journal papers published	35	17	10
2	No. of peer reviewed conference papers published	14	7	6
3	No. of books/book chapters published	3	2	0

C7. Sponsored Research Project

Table No. C7.1: List of sponsored research projects received from external agencies.

(CAYm1)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr. T. Jayachandra Prasad	Dr. P. V. Gopi Krishna Rao	ECE, RGMCET	Idea Lab	AICTE, New Delhi	2 Years	90.00
						Amount received (Rs.):90.00

(CAYm2)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
0	0	0	0	0	0	0.00
						Amount received (Rs.):0.00

(CAYm3)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
0	0	0	0	0	0	0.00
						Amount received (Rs.):0.00

Total Amount (Lacs) Received for the Past 3 Years: 90.00

Note*:

- Only sponsored research projects will be considered. Infrastructure-based projects will not be considered here.

C8. Consultancy Work

Table No. C8.1: List of consultancy projects received from external agencies.

(CAYm1)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr. P. V. Gopi Krishna Rao	Dr. R. Hanuma Naik	ECE, RGMCET	Design and Development of Milk Filtering System suitable for Farm Scale Automated Operations	Bhat & Bhat Farms	1 Year	10.00
						Amount received (Rs.):10.00

(CAYm2)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr.Y.Madhu Sudhana Reddy	Dr. R. Hanuma Naik	ECE, RGMCET	Solar Dryers with internet of thinks (IoT0 and Artificial Intelligence (AI)	Vayuputra Horticulture Farmers MACS	1 Year	1.50
						Amount received (Rs.):1.50

(CAYm3)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr. P. V. Gopi Krishna Rao	Dr. Y. Madhu Sudhana Reddy	ECE, RGMECT	Design and Development of Exam Automation System	KSRM COLLEGE OF ENGINEERING	1 Year	0.75
Dr. P.V.Gopi Krishna Rao	Mr.N.Nagaraja Kumar	ECE, RGMCET	Updradation of Sloar Light Switching System	Sannidhi Systems,Vijayawada	1 Year	0.19
						Amount received (Rs.):0.94

Total amount (Lacs) received for the past 3 years: 12.44

Note*:

- Only consultancy projects will be considered. Infrastructure-based projects will not be considered here.

C9. Institution Seed Money or Internal Research Grant to its Faculty for Research Work

Table No. C9.1: List of faculty members received seed money or internal research grant from the Institution.

(CAYm1)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
P.Chandra Sekhar	Development of a Low-Cost Microwave Weed Control System	12 Months	2.50	2.51	-
			Amount received (Rs.): 2.50		

(CAYm2)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Dr. P. V. Gopi Krishna Rao	Connected Care for All an Intelligent Virtual Health Ecosystem for Rural and Remote Populations	12 Months	2.04	2.04	-
			Amount received (Rs.): 2.04		

(CAYm3)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Dr. P. V. Gopi Krishna Rao	Ternary logic Full Adder Circuit using 3x1 Multiplexer	12 Months	4.00	4.00	-
Dr.M.Chennakesavulu	Performance Analysis of Full Adder using Ganged CMOS Threshold Element with Different Technologies	12 Months	4.00	4.00	-
			Amount received (Rs.): 8.00		

Total amount (Lacs) received for the past 3 years : 12.54

PART D: Laboratory Infrastructure in the Department

(Data to be filled in for the Department)

D1. Adequate and Well-Equipped Laboratories, and Technical Manpower

Table No.D1.1: List of laboratories and technical manpower.

Sr. No	Name of the Laboratory	Number of students per set up(Batch Size)	Name of the Important Equipment	Weekly utilization status(all the courses for which the lab is utilized)	Technical Manpower Support		
					Name of the Technical staff	Designation	Qualification
1	Electronics Engineering Workshop	3	CRO-22	36	K. Seenibabu	Lab Assistant	DECE
2	Electrical and Electronics Engineering workshop	3	Regulated Power Supplies-43	36	K. Seenibabu	Lab Assistant	DECE
3	Network Analysis and Simulation Lab	3	CRO-25	12	V.Hari Hara Prasad Reddy	Lab Assistant	DECE
4	Network Analysis and Simulation Lab	3	Regulated Power Supplies-27	12	V.Hari Hara Prasad Reddy	Lab Assistant	DECE
5	Network Analysis and Simulation Lab, VLSI Design Lab, PCB Design and Prototype	1	Computers-68	24	C. Ravi Kumar	Lab Assistant	M.Sc (CS)
6	Digital Design and Signal Simulation Lab Basic Simulation Lab Tinkering Lab and	1	Computers-70	24	C. Ravi Kumar	Lab Assistant	M.Sc (CS)
7	Advanced Electronic Circuits Design Lab	3	DSOs.-12	18	G.Y. Subramanyam	Lab Assistant	DECE
8	Advanced Electronic Circuits Design Lab	3	Function Generator-12	18	G.Y. Subramanyam	Lab Assistant	DECE
9	Advanced Electronic Circuits Design Lab	3	Regulated Power Supplies-12.	18	G.Y. Subramanyam	Lab Assistant	DECE
10	Analog and Digital IC Applications Lab	3	DSOs-12	18	G.Y. Subramanyam	Lab Assistant	DECE
11	Analog and Digital IC Applications Lab	3	Regulated Power Supplies-12	18	G.Y. Subramanyam	Lab Assistant	DECE
12	Analog & Digital IC applications Lab	3	DSOs-12	18	G.Y. Subramanyam	Lab Assistant	DECE
13	Analog & Digital IC applications Lab	3	Function Generators-12	18	G.Y. Subramanyam	Lab Assistant	DECE
14	Analog & Digital IC applications Lab	3	Regulated Power Supplies-12	18	G.Y. Subramanyam	Lab Assistant	DECE
15	Tinkering Lab	1	Arduino Boards-60	18	C.Ravi Kumar	Lab Assistant	M.Sc (CS)

16	Microwave and Optical Communications Lab	3	Microwave Benches-06	18	G.Y. Subramanyam	Lab Assistant	DECE
17	Microwave and Optical Communications Lab	3	Microwave benches -01 (GUNN Diode based)	18	G.Y. Subramanyam	Lab Assistant	DECE
18	Microwave and Optical Communications Lab	3	Optical Communication Trainer kits.-04	18	G.Y. Subramanyam	Lab Assistant	DECE
19	Microwave and Optical Communications Lab	3	Klystron power supply-08	18	G.Y. Subramanyam	Lab Assistant	DECE
20	Microwave and Optical Communications Lab	3	Klystron mount-08	18	G.Y. Subramanyam	Lab Assistant	DECE
21	Microwave and Optical Communications Lab	3	Isolator-08	18	G.Y. Subramanyam	Lab Assistant	DECE
22	Microwave and Optical Communications Lab	3	Circulator-05	18	G.Y. Subramanyam	Lab Assistant	DECE
23	Microwave and Optical Communications Lab	3	Frequency meter-08	18	G.Y. Subramanyam	Lab Assistant	DECE
24	Microwave and Optical Communications Lab	3	Variable attenuator-08	18	G.Y. Subramanyam	Lab Assistant	DECE
25	Microwave and Optical Communications Lab	3	CROs-05	18	G.Y. Subramanyam	Lab Assistant	DECE
26	Microwave and Optical Communications Lab	3	Function Generators-05	18	G.Y. Subramanyam	Lab Assistant	DECE
27	Microwave and Optical Communications Lab	3	Fixed attenuators-06	18	G.Y. Subramanyam	Lab Assistant	DECE
28	Microwave and Optical Communications Lab	3	Slotted section-05	18	G.Y. Subramanyam	Lab Assistant	DECE
29	Microwave and Optical Communications Lab	3	Tunable probe-05	18	G.Y. Subramanyam	Lab Assistant	DECE
30	Microwave and Optical Communications Lab	3	Detector mount-08	18	G.Y. Subramanyam	Lab Assistant	DECE
31	Microwave and Optical Communications Lab	3	Tripod stand-1	18	G.Y. Subramanyam	Lab Assistant	DECE
32	Microwave and Optical Communications Lab	3	Magic tee-04	18	G.Y. Subramanyam	Lab Assistant	DECE

33	Microwave and Optical Communications Lab	3	Directional coupler-04	18	G.Y. Subramanyam	Lab Assistant	DECE
34	Microwave and Optical Communications Lab	3	Gunn power supply-02	18	G.Y. Subramanyam	Lab Assistant	DECE
35	Microwave and Optical Communications Lab	3	Gunn oscillator-02	18	G.Y. Subramanyam	Lab Assistant	DECE
36	Microwave and Optical Communications Lab	3	Laser diode/LED characteristics kit-01	18	G.Y. Subramanyam	Lab Assistant	DECE
37	Microwave and Optical Communications Lab	3	Fiber optical trainer kit-03	18	G.Y. Subramanyam	Lab Assistant	DECE
38	Microwave and Optical Communications Lab	3	Optical laser kit-01	18	G.Y. Subramanyam	Lab Assistant	DECE
39	Microwave and Optical Communications Lab	3	VSWR meter-06	18	G.Y. Subramanyam	Lab Assistant	DECE
40	Microwave and Optical Communications Lab	3	RF signal generator-01	18	G.Y. Subramanyam	Lab Assistant	DECE
41	Analog and Digital Communication Lab	3	CROs-20	18	K. Seenibabu	Lab Assistant	DECE
42	Analog and Digital Communication Lab	3	DSOs-11	18	K. Seenibabu	Lab Assistant	DECE
43	Analog and Digital Communication Lab	3	Function Generators-06	18	K. Seenibabu	Lab Assistant	DECE
44	Analog and Digital Communication Lab	3	Pulse Generators-03	18	K. Seenibabu	Lab Assistant	DECE
45	Analog and Digital Communication Lab	3	Arbitrary FGs-02	18	K. Seenibabu	Lab Assistant	DECE
46	Analog and Digital Communication Lab	3	Regulated Power Supplies-28	18	K. Seenibabu	Lab Assistant	DECE
47	Analog and Digital Communication Lab	3	Decade resistance box-12	18	K. Seenibabu	Lab Assistant	DECE
48	Analog and Digital Communication Lab	3	Decade capacitance box-12	18	K. Seenibabu	Lab Assistant	DECE
49	Analog and Digital Communication Lab	3	Decade inductance box-12	18	K. Seenibabu	Lab Assistant	DECE

50	Analog and Digital Communication Lab	3	Digital multimeter-8	18	K. Seenibabu	Lab Assistant	DECE
51	Analog and Digital Communication Lab	3	IC tester-1	18	K. Seenibabu	Lab Assistant	DECE
52	Analog and Digital Communication Lab	3	Audio generator-1	18	K. Seenibabu	Lab Assistant	DECE
53	Analog and Digital Communication Lab	3	Programmable function generator-1	18	K. Seenibabu	Lab Assistant	DECE
54	Analog and Digital Communication Lab	3	Wireless equipment-1	18	K. Seenibabu	Lab Assistant	DECE
55	Analog and Digital Communication Lab	3	Synthesized function generator-1	18	K. Seenibabu	Lab Assistant	DECE
56	Analog and Digital Communication Lab(Simulation)	3	Computers-3	18	C.Ravi Kumar	Lab Assistant	M.Sc (CS)
57	Analog and Digital Communication Lab (Simulation)	3	MATLAB 9.0 (R2016B)-50 Users	18	C.Ravi Kumar	Lab Assistant	M.Sc (CS)
58	Digital logic design	3	Regulated Power Supply-10	18	G.Y. Subramanyam	Lab Assistant	DECE
59	Digital logic design	3	Multimeters-5	18	G.Y. Subramanyam	Lab Assistant	DECE
60	Digital logic design	3	Decade resistance box-11	18	G.Y. Subramanyam	Lab Assistant	DECE
61	Digital logic design	3	Decade capacitance box-15	18	G.Y. Subramanyam	Lab Assistant	DECE
62	Digital logic design	3	Decade inductance box-12	18	G.Y. Subramanyam	Lab Assistant	DECE
63	Digital logic design	3	LCR Q meter-1	18	G.Y. Subramanyam	Lab Assistant	DECE
64	Digital logic design	3	CROs-17	18	G.Y. Subramanyam	Lab Assistant	DECE
65	Digital logic design	3	AC ammeter-17	18	G.Y. Subramanyam	Lab Assistant	DECE
66	Digital logic design	3	AC voltmeter-2	18	G.Y. Subramanyam	Lab Assistant	DECE

67	Digital logic design	3	DC ammeter-34	18	G.Y. Subramanyam	Lab Assistant	DECE
68	Digital logic design	3	DC voltmeter-21	18	G.Y. Subramanyam	Lab Assistant	DECE
69	Electronic Circuit Analysis Lab (Simulation)	1	Computers-35	18	C. Ravi Kumar	Lab Assistant	M.Sc (CS)
70	Basic Simulation Lab	1	Computers-70	18	C. Ravi Kumar	Lab Assistant	M.Sc (CS)
71	Basic Simulation Lab	1	MATLAB 9.0(R2016B)-50 Users	18	C. Ravi Kumar	Lab Assistant	M.Sc (CS)
72	Digital signal processing lab	1	Computer Server-01	18	C. Ravi Kumar	Lab Assistant	M.Sc (CS)
73	Digital signal processing lab	1	MATLAB 9.0(R2016B)	18	C. Ravi Kumar	Lab Assistant	M.Sc (CS)
74	Electronic Devices and Circuits Lab, Electronic Circuit Analysis lab	3	Regulated power supply-27	18	V. Hari Hara Prasad Redd	Lab Assistant	DECE
75	Electronic Devices and Circuits Lab, Electronic Circuit Analysis lab	3	CROs-25	18	V. Hari Hara Prasad Redd	Lab Assistant	DECE
76	Electronic Devices and Circuits Lab, Electronic Circuit Analysis lab	3	Function Generators-14	18	V. Hari Hara Prasad Redd	Lab Assistant	DECE
77	Electronic Devices and Circuits Lab, Electronic Circuit Analysis lab	3	Decade resistance box-31	18	V. Hari Hara Prasad Redd	Lab Assistant	DECE
78	Electronic Devices and Circuits Lab, Electronic Circuit Analysis lab	3	Decade capacitance box-22	18	V. Hari Hara Prasad Redd	Lab Assistant	DECE
79	Electronic Devices and Circuits Lab, Electronic Circuit Analysis lab	3	Decade inductance box-17	18	V. Hari Hara Prasad Redd	Lab Assistant	DECE
80	Electronic Devices and Circuits Lab, Electronic Circuit Analysis lab	3	Digital multi meters-05	18	V. Hari Hara Prasad Redd	Lab Assistant	DECE
81	Electronic Devices and Circuits Lab, Electronic Circuit Analysis lab	3	AC ammeters-60	18	V. Hari Hara Prasad Redd	Lab Assistant	DECE
82	Electronic Devices and Circuits Lab, Electronic Circuit Analysis lab	3	AC voltmeters-22	18	V. Hari Hara Prasad Redd	Lab Assistant	DECE
83	Electronic Devices and Circuits Lab, Electronic Circuit Analysis lab	3	DC ammeters-172	18	V. Hari Hara Prasad Redd	Lab Assistant	DECE

84	Electronic Devices and Circuits Lab, Electronic Circuit Analysis lab	3	DC voltmeters-82	18	V. Hari Hara Prasad Redd	Lab Assistant	DECE
85	Electronic Devices and Circuits Lab, Electronic Circuit Analysis lab	3	Electronic circuit trainer kits-35	18	V. Hari Hara Prasad Redd	Lab Assistant	DECE
86	Electronic Devices and Circuits Lab, Electronic Circuit Analysis lab	3	Electronic devices trainer kits-25	18	V. Hari Hara Prasad Redd	Lab Assistant	DECE
87	Electronic Devices and Circuits Lab, Electronic Circuit Analysis lab	3	IC tester-1	18	V. Hari Hara Prasad Redd	Lab Assistant	DECE
88	Microprocessor and micro controller lab	3	8085Microprocessor kits-15	24	G.Y. Subramanyam	Lab Assistant	DECE
89	Microprocessor and Microcontrollers Lab	3	8086 Microprocessor Kits-15	24	G.Y. Subramanyam	Lab Assistant	DECE
90	Microprocessor and micro controller lab	3	8051Microcontroller kit-15	24	G.Y. Subramanyam	Lab Assistant	DECE
91	Microprocessor and micro controller lab	3	CROs-4	24	G.Y. Subramanyam	Lab Assistant	DECE
92	Microprocessor and micro controller lab	3	Interfacing Boards-18	24	G.Y. Subramanyam	Lab Assistant	DECE
93	Microprocessor and micro controller lab	3	Multimeters-3	24	G.Y. Subramanyam	Lab Assistant	DECE
94	Microprocessor and micro controller lab	3	IC tester-1	24	G.Y. Subramanyam	Lab Assistant	DECE
95	Microprocessor and micro controller lab	3	8 digit multiplexed display card-04	24	G.Y. Subramanyam	Lab Assistant	DECE
96	Microprocessor and micro controller lab	3	Stepper motor-10	24	G.Y. Subramanyam	Lab Assistant	DECE
97	Microprocessor and micro controller lab	3	8251/8253 interfacing board-06	24	G.Y. Subramanyam	Lab Assistant	DECE
98	Microprocessor and micro controller lab	3	8255 interfacing board-06	24	G.Y. Subramanyam	Lab Assistant	DECE
99	Microprocessor and micro controller lab	3	8259 interfacing board-06	24	G.Y. Subramanyam	Lab Assistant	DECE
100	Microprocessor and micro controller lab	3	AD590 transducer-04	24	G.Y. Subramanyam	Lab Assistant	DECE

101	Microprocessor and micro controller lab	3	DMA interfacing board-04	24	G.Y. Subramanyam	Lab Assistant	DECE
102	Microprocessor and micro controller lab	3	ADC interfacing boards-05	24	G.Y. Subramanyam	Lab Assistant	DECE
103	Microprocessor and micro controller lab	3	DAC interfacing boards-04	24	G.Y. Subramanyam	Lab Assistant	DECE
104	Microprocessor and micro controller lab	3	Elevator simulator-06	24	G.Y. Subramanyam	Lab Assistant	DECE
105	Microprocessor and micro controller lab	3	EEPROM program device-06	24	G.Y. Subramanyam	Lab Assistant	DECE
106	Microprocessor and micro controller lab	3	Keyboard display device-06	24	G.Y. Subramanyam	Lab Assistant	DECE
107	Microprocessor and micro controller lab	3	Printer interface board-04	24	G.Y. Subramanyam	Lab Assistant	DECE
108	Microprocessor and micro controller lab	3	Real time clock-06	24	G.Y. Subramanyam	Lab Assistant	DECE
109	Microprocessor and micro controller lab	3	Traffic light simulator-06	24	G.Y. Subramanyam	Lab Assistant	DECE
110	Linear and digital integrated circuits lab	3	Function generators-14	24	V. Hari Hara Prasad Redd	Lab Assistant	DECE
111	Linear and digital integrated circuits lab	3	CROs-22	24	V. Hari Hara Prasad Redd	Lab Assistant	DECE
112	Linear and digital integrated circuits lab	3	Regulated Power Supply-43	24	V. Hari Hara Prasad Redd	Lab Assistant	DECE
113	Linear and digital integrated circuits lab	3	Decade resistance box-23	24	V. Hari Hara Prasad Redd	Lab Assistant	DECE
114	Linear and digital integrated circuits lab	3	Decade capacitance box-34	24	V. Hari Hara Prasad Redd	Lab Assistant	DECE
115	Linear and digital integrated circuits lab	3	Decade inductance box-23	24	V. Hari Hara Prasad Redd	Lab Assistant	DECE
116	Linear and digital integrated circuits lab	3	Analog IC trainer kit-04	24	V. Hari Hara Prasad Redd	Lab Assistant	DECE
117	ARM programming Lab, CMOS Analog IC Design Lab,CMOS Digital IC Design	1	Computers-35	24	C. Ravi Kumar	Lab Assistant	M.Sc (CS)

118	ARM programming Lab	1	ARM kits -35	24	C. Ravi Kumar	Lab Assistant	M.Sc (CS)
119	ARM programming Lab	1	Network switching equipment	24	C. Ravi Kumar	Lab Assistant	M.Sc (CS)
120	VLSI Design Lab, CMOS Analog IC Design Lab, CMOS Digital IC Design Lab, Mixed	1	Cadence Software-35 Users	24	C. Ravi Kumar	Lab Assistant	M.Sc (CS)
121	Analog & Digital communications Lab	3	Digital communication kits-79	18	K.Sreenibabu	Lab Assistant	DECE
122	Analog & Digital communications Lab	3	Digital Multi meter-8	18	K.Sreenibabu	Lab Assistant	DECE
123	Digital Logic Design Lab	3	Lab Trainer Kits-3	18	G.Y.Subramanyam	Lab Assistant	DECE
124	Digital Logic Design Lab	3	Analog & Digital Trainer Kits-10	18	G.Y.Subramanyam	Lab Assistant	DECE

D2. Safety Measures in Laboratories

Table No. D2.1: List of various safety measures in laboratories.

Sr. No	Laboratory Name	Safety Measures
1	Hardware Laboratories (Analog and Digital Communications Lab, Electrical and Electronics Engineering Workshop, Digital Logic Design Lab, Electronic Circuit Analysis Lab, Electronic Devices and	1) Each laboratory and floor is equipped with appropriate fire extinguishers (CO_2 / Dry Chemical Type). The extinguishers are periodically inspected and maintained as per safety norms, and inspection records are documented 2) A fully stocked First Aid Box is available in each laboratory. The contents are periodically checked and replenished. 3) Laboratories are designed with adequate ventilation and airflow systems to prevent overheating of equipment and ensure a safe working environment. 4) All laboratories are equipped with properly rated MCBs/ELCBs and circuit breakers to protect equipment and users from electrical faults, overload, and short circuits. Proper earthing is ensured for all equipment. 5) Worktables are kept clean and organized. All instruments and components are properly arranged in designated racks after use to prevent hazards. 6) Students are permitted to operate laboratory equipment only under the supervision of trained laboratory staff or authorized faculty members. 7) Performing unauthorized experiments or modifying equipment without prior approval is strictly prohibited. 8) Students are strictly prohibited from working alone in the laboratory. At least one authorized staff member must be present during laboratory hours. 9) Laboratory safety rules and procedures are clearly displayed inside the laboratory. 10) Students are briefed on laboratory safety procedures at the beginning of each semester. 11) Any accident or equipment damage must be immediately reported to the concerned faculty/lab in-charge.
2	Software Laboratories (Basic Simulation Lab, Digital Signal Processing lab, VLSI Design Lab, PCB Design and Prototype Development Lab)	1) Each laboratory and floor is equipped with appropriate fire extinguishers (CO_2 / Dry Chemical Type). The extinguishers are periodically inspected and maintained as per safety norms, and inspection records are documented 2) A fully stocked First Aid Box is available in each laboratory. The contents are periodically checked and replenished. 3) Laboratories are designed with adequate ventilation and airflow systems to prevent overheating of equipment and ensure a safe working environment. 4) All laboratories are equipped with properly rated MCBs/ELCBs and circuit breakers to protect equipment and users from electrical faults, overload, and short circuits. Proper earthing is ensured for all equipment. Servo Stabilizers are available for the labs 5) Students must enter the laboratory only in the presence of the instructor/lab in-charge. 6) Bags should be kept in designated areas to avoid obstruction. 7) Do not touch electrical switches, plugs, or equipment with wet hands. Report any damaged cables, loose connections, or sparking immediately. Avoid overloading power sockets and extension boards. 8) Do not install unauthorized software or use external storage devices without permission. Avoid accessing unsafe or irrelevant websites. 9) Maintain confidentiality of login credential.

D3. Project Laboratory/Research Laboratory

List of project laboratory/research laboratory /Centre of Excellence

Nano Scale Device Simulation Lab

1. Overview:

The **Nano Scale Device Simulation Laboratory** is an advanced computational facility established to support teaching, research, and project work in the area of nanoscale semiconductor devices and quantum electronics. The laboratory enables simulation, modelling, and analysis of nano electronic devices using industry-standard TCAD and quantum simulation tools.

The lab is primarily utilized by **IV Year B.Tech students and M.Tech students** for academic laboratory sessions, mini projects, major projects, and research-oriented work.

2. Objectives of the Laboratory

- To provide hands-on experience in nanoscale device modeling and simulation.
- To bridge the gap between theoretical semiconductor physics and practical device behavior.
- To enable students to analyze nano-scale MOSFETs, FinFETs, Tunnel FETs, and quantum devices.
- To promote research and innovation in nanoelectronics and advanced semiconductor technologies.
- To facilitate project-based learning for final-year and postgraduate students.

3. Infrastructure and Computing Facilities

The laboratory is equipped with:

- High-end computing systems with advanced processors and high RAM capacity to handle complex simulations.
- Licensed simulation software for device-level and atomistic-level analysis.
- Dedicated network access and data storage facilities.
- UPS backup and uninterrupted power supply.
- Proper ventilation and structured workspace arrangement.

4. Software Tools Available

The laboratory is equipped with the following advanced simulation tools:

- **Visual TCAD**

  Used for Technology Computer-Aided Design (TCAD) simulation of semiconductor devices. It supports process simulation, device modeling, and electrical characterization.

- **ATOMIX Quantum Toolkit**

  A quantum simulation platform used for atomistic modeling, nanoscale device simulation, and first-principles calculations based on quantum mechanics.

These tools enable students to perform:

- Device structure design
- Doping profile analysis
- Electrical characteristics extraction
- Quantum transport simulations
- Band structure analysis
- Nanoscale transistor modeling

5. Academic Utilization

The lab supports:

- IV Year B.Tech Major Projects
- M.Tech Dissertation Work
- Advanced Laboratory Courses

- Research Methodology Training
- Simulation-Based Design Projects

Students undertake simulation-based design and analysis of:

- Nano MOSFETs
- FinFETs
- TFETs
- Quantum devices
- 2D material-based devices

6. Outcome Contribution

The Nano Scale Device Simulation Laboratory contributes to:

- Program Outcomes related to problem analysis, design, investigation, and modern tool usage.
- Enhancement of computational and research skills.
- Preparation of students for higher studies and semiconductor industry roles.
- Publication of research papers and participation in technical conferences.

Embedded Systems and Robotics Laboratory Lab

1. Overview:

The Embedded Systems and Robotics Laboratory is established to provide hands-on training in the design, development, and implementation of embedded hardware and software systems. The laboratory enables students to integrate microcontrollers, processors, sensors, and communication modules for real-time applications.

The lab supports undergraduate (III/IV Year B.Tech) and postgraduate (M.Tech) students for laboratory courses, mini-projects, and major project work.

2. Objectives of the Laboratory:

- To provide practical exposure to embedded system design and development.
- To develop programming skills using embedded C and real-time operating concepts.
- To enable interfacing of peripherals such as sensors, actuators, displays, and communication modules.
- To design real-time applications using microcontrollers and development boards.
- To promote project-based learning aligned with industry requirements.

3. Infrastructure and Facilities

The laboratory is equipped with:

- Adequate number of workstations and development kits.
- High-end desktop systems for programming and debugging.
- Licensed/open-source embedded development environments.
- Structured work benches with regulated power supply.
- Internet connectivity for firmware updates and documentation access.
- UPS backup and proper electrical earthing.
- Adequate ventilation and safety compliance measures.

4. Major Equipment and Development Platforms

The laboratory consists of:

- Microcontroller development boards (8-bit / 16-bit / 32-bit)

- ARM Cortex-based development kits
- FPGA/SoC-based kits (if available)
- Sensor modules (Temperature, IR, Ultrasonic, etc.)
- Motor driver modules
- LCD / OLED display modules
- Communication modules (UART, I2C, SPI, Bluetooth, Wi-Fi)
- Digital Storage Oscilloscope (DSO)
- Function Generators
- Regulated Power Supplies

5. Software Tools

The laboratory supports embedded development using:

- Embedded C Programming
- IDE-based firmware development platforms
- Debugging and flashing tools
- Simulation tools (if applicable)
- RTOS-based application development (for PG level)

6. Academic Utilization

The laboratory supports:

- Embedded Systems Laboratory Course
- Microcontroller & Interfacing Lab
- Real-Time Systems Lab (PG)
- Mini Projects (III Year)
- Major Projects (IV Year)
- M.Tech Dissertation Work

Students implement applications such as:

- LED and LCD interfacing
- Sensor-based monitoring systems
- Motor control systems
- IoT-based monitoring systems
- Serial communication protocols
- Real-time task scheduling applications

Internet of Things (IOT) Lab

1. Overview:

The Internet of Things (IoT) Laboratory is established to provide hands-on training in the design, development, and deployment of IoT-based systems integrating sensors, embedded platforms, communication networks, and cloud technologies.

The laboratory supports undergraduate (IV Year B.Tech) and postgraduate (M.Tech) students for laboratory courses, mini-projects, major projects, and research activities.

The lab facilitates experiential learning aligned with industry requirements in smart systems, automation, and connected technologies.

2. Objectives of the Laboratory

- To provide practical exposure to IoT system architecture and design.
- To enable integration of sensors, microcontrollers, and cloud platforms.
- To develop skills in wireless communication protocols.
- To design real-time monitoring and control applications.
- To promote innovation and project-based learning in emerging IoT domains.

3. Infrastructure and Facilities

The laboratory is equipped with:

- High-end computing systems for IoT programming and simulation.
- IoT development boards and embedded platforms.
- Sensor and actuator modules.
- Wireless communication modules.
- High-speed internet connectivity.
- Structured work benches with regulated power supply.
- UPS backup and proper electrical earthing.
- Adequate ventilation and safety measures.

4. Major Equipment and Development Platforms

The laboratory consists of:

- Microcontroller-based IoT development boards (ESP32 / NodeMCU / Arduino-based platforms)
- Single-board computers (Raspberry Pi or equivalent)
- Sensor modules (Temperature, Humidity, Gas, IR, Ultrasonic, Motion, etc.)
- Actuator modules (Relay, DC motor, Servo motor, Stepper motor)
- Wi-Fi / Bluetooth / Zigbee communication modules
- GSM/GPRS modules (if available)
- IoT gateway devices
- Digital Storage Oscilloscope (DSO)
- Regulated DC Power Supplies

5. Software and Development Tools

The lab supports IoT application development using:

- Embedded C / Python programming
- Arduino IDE / PlatformIO / Similar development environments
- MQTT and HTTP-based communication frameworks
- Cloud platforms for IoT data visualization and storage
- Database and dashboard development tools

6. Academic Utilization

The IoT Laboratory supports:

- IoT Laboratory Course (UG)

- Advanced IoT / Smart Systems Lab (PG)
- IV Year Major Projects
- M.Tech Dissertation Work
- Industry-oriented mini projects
- Hackathon and innovation activities

Students implement applications such as:

- Smart Home Automation Systems
- Smart Agriculture Monitoring
- Environmental Monitoring Systems
- Health Monitoring Systems
- Industrial IoT Applications
- Energy Management Systems
- IoT-based Security Systems

The lab enhances industry readiness in domains such as:

- Smart Systems
- Automation
- Embedded IoT Firmware Development
- Cloud-based Data Analytics

Industry 4.0 Applications

AICTE IDEA Lab/FAB Lab

(Innovation, Design & Entrepreneurship Academy Laboratory)

1. Introduction

The **AICTE IDEA Lab** (Innovation, Design & Entrepreneurship Academy Lab) is established as per the guidelines of All India Council for Technical Education to promote interdisciplinary learning, hands-on training, design thinking, prototyping, and innovation among students.

The lab serves as a centralized facility accessible to students of all departments to conceptualize, design, fabricate, test, and validate prototypes for academic, research, and entrepreneurial activities.

2. Objectives of AICTE IDEA Lab

1. To promote design thinking and problem-solving skills
2. To encourage interdisciplinary project development
3. To facilitate rapid prototyping and product development
4. To support start-up and entrepreneurship initiatives
5. To enhance industry interaction and real-world exposure

3. Major Equipment Available

A. Design & Modeling Tools

- High-end CAD workstations
- Licensed software (AutoCAD/SolidWorks/ANSYS or equivalent)

B. Rapid Prototyping Equipment

- 3D Printers (FDM/SLA based)
- 3D Scanner

- Laser Cutting Machine
- CNC Milling Machine

C. Electronics & IoT Section

- PCB Prototyping Machine
- Soldering & Rework Stations
- Oscilloscopes
- Function Generators
- Microcontroller & IoT Kits (Arduino, Raspberry Pi, etc.)

D. Mechanical Fabrication Tools

- Drilling Machine
- Grinding Machine
- Lathe Machine (Mini)
- Hand tools & measuring instruments

4. Utilization of IDEA Lab

Academic Integration

- Mini Projects
- Major Projects
- Capstone Projects
- Internship-based projects

Skill Development

- Workshops on:
  - 3D Printing
  - IoT & Embedded Systems
  - Robotics
  - Product Design
- Value-added certification programs

Innovation & Entrepreneurship

- Prototype development for start-ups
- Support to Innovation/Incubation Cell
- Participation in Smart India Hackathon and similar competitions

5. Interdisciplinary Usage

Students from:

- Mechanical Engineering
- Electrical Engineering
- Electronics & Communication Engineering
- Computer Science Engineering

- Civil Engineering utilize the lab for collaborative projects.

The AICTE IDEA Lab/FAB Lab acts as a hub of creativity, innovation, and product realization, enabling students to transform ideas into functional prototypes. It strengthens experiential learning, interdisciplinary collaboration, and entrepreneurial mindset, aligning with the vision of All India Council for Technical Education and the quality benchmarks expected during NBA accreditation.

PART E: First Year faculty and financial Resources

(Data to be filled in for the first year course faculty and budget allocation and utilization)

E1. First Year Student-Faculty Ratio (FYSFR)

Table No. E1.1: FYSFR details.

Year	Sanctioned intake of all UG programs (S4)	No. of required faculty (RF4= S4/20)	No. of faculty members in Basic Science Courses & Humanities and Social Sciences including Management courses (NS1)	No. of faculty members in Engineering Science Courses (NS2)	Percentage= No. of faculty members ((NS1*0.8) + (NS2*0.2))/(No. of required faculty (RF4)); Percentage= ((NS1*0.8) +(NS2*0.2))/RF
2023-24(CAYm2)	1140	57	55	61	99
2024-25(CAYm1)	1620	81	66	75	84
2025-26(CAY)	1620	81	71	82	90

E2. Budget Allocation, Utilization, and Public Accounting at Institute Level

Table No. E2.1: Budget and actual expenditure incurred at Institute level.

Items	Budgeted in 2025-26	Actual Expenses in 2025-26 till	Budgeted in 2024-25	Actual Expenses in 2024-25 till	Budgeted in 2023-24	Actual Expenses in 2023-24 till	Budgeted in 2022-23	Actual Expenses in 2022-23 till
Infrastructure Built-Up	106100000	145423427	79800000	70110332	86400000	78793124	58400000	58116805
Library	2650000	1872314	2780000	2463980	1250000	2868937	1500000	1379922
Laboratory equipment	22000000	9767365	23290000	21522840	12500000	20406549	10000000	8640767
Teaching and non-teaching staff salary	353000000	261227078	315000000	312747669	300473157	282310639	265000000	262794339
Outreach Programs	4250000	2940185	3050000	2861966	2350000	4134015	1950000	2097577
R&D	3000000	0	3500000	501150	10960000	475320	1000000	325180
Training, Placement and Industry linkage	20500000	10939919	8000000	9769805	2000000	7597769	11500000	14773492
SDGs	203290000	157984116	250020000	255565128	41275000	45231455	38850000	38781182
Entrepreneurship	520000	494041	270000	378272	800000	484562	700000	641267

Others, specify	66574302	34571709	71710000	70662066	55758000	56417142	50830000	48675866
Total	**781884302**	**625220154**	**757420000**	**746583208**	**513766157**	**498719512**	**439730000**	**436226397**

E3. Budget Allocation, Utilization, and Public Accounting at Program Specific Level

Table No. E3.1: Budget and actual expenditure incurred at program level.

Items	**Budgeted in 2025-26**	**Actual Expenses in 2025-26 till**	**Budgeted in 2024-25**	**Actual Expenses in 2024-25 till**	**Budgeted in 2023-24**	**Actual Expenses in 2023-24 till**	**Budgeted in 2022-23**	**Actual Expenses in 2022-23 till**
Laboratory equipment	1000000	950275	2500000	2383674	4100000	4137333	3500000	3539890
Software	1200000	1062000	0	0	0	0	0	0
SDGs	675000	233998	550000	572170	925000	1251522	560000	81138
Support for faculty development	200000	194019	75000	59076	50000	46877	75000	84355
R & D	355000	315000	250000	250632	250000	204165	900000	800000
Industrial Training, Industry expert, Internship	425000	422360	125000	125000	125000	125000	125000	125000
Miscellaneous Expenses*	0	0	0	0	0	0	0	0
Total	**3855000**	**3177652**	**3500000**	**3390552**	**5450000**	**5764897**	**5160000**	**4630383**